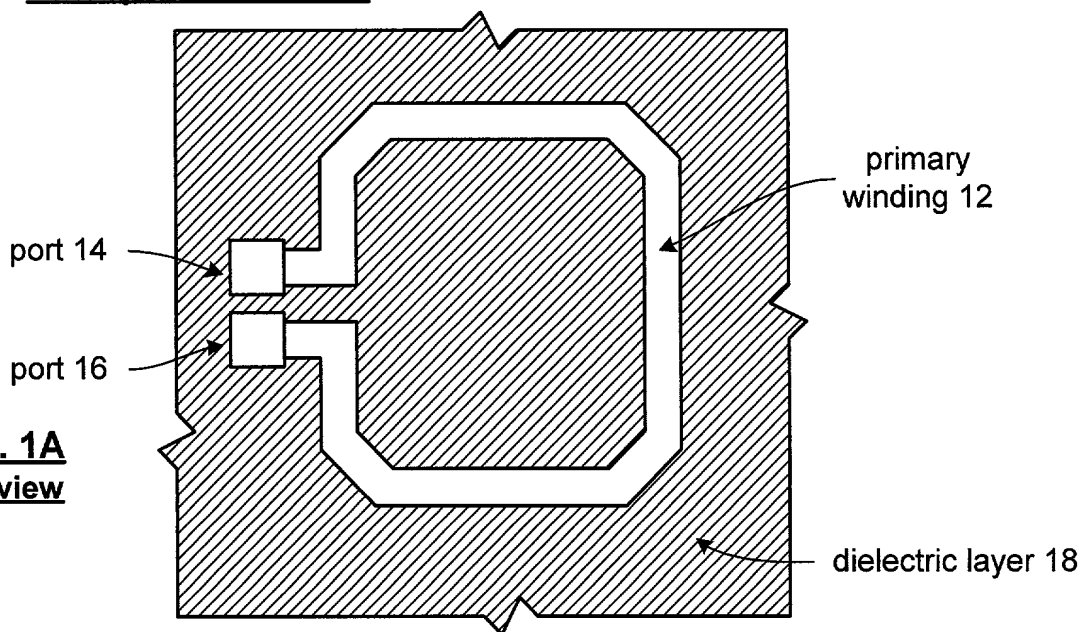
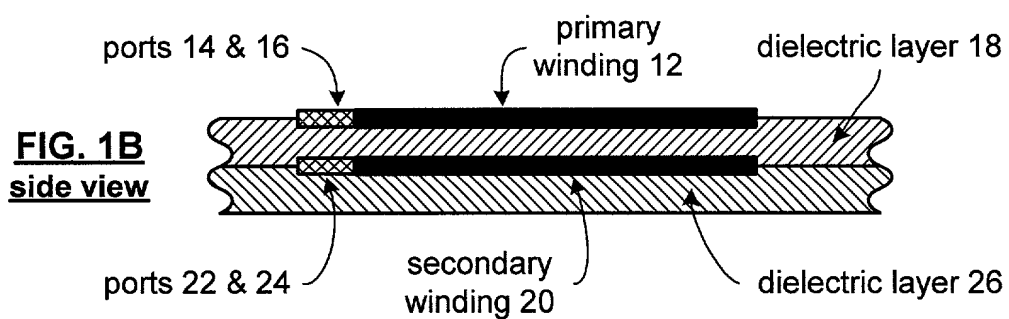


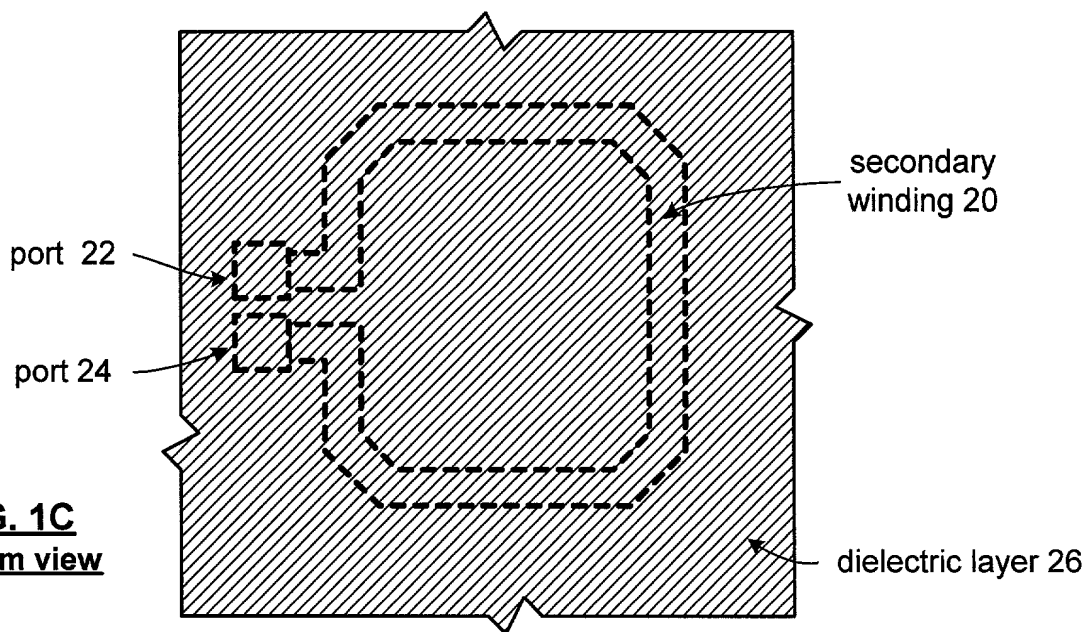
on-chip transformer 10



**FIG. 1A**  
**top view**



**FIG. 1B**  
**side view**

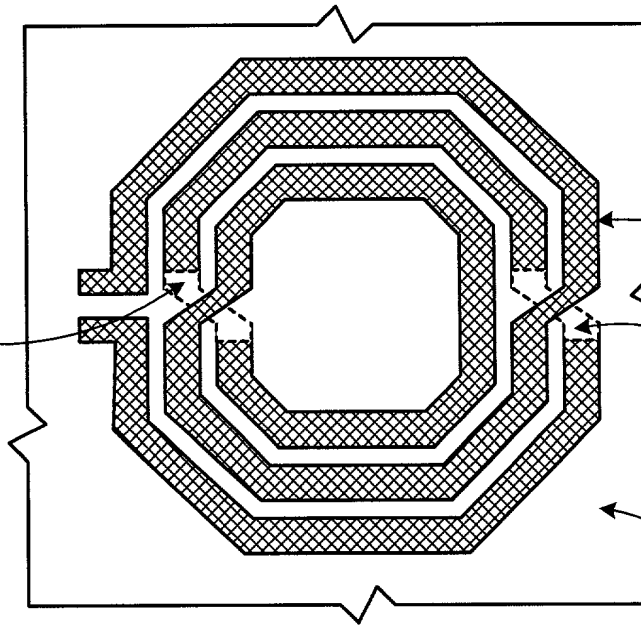


**FIG. 1C**  
**bottom view**

**on-chip  
transformer 30**

**FIG. 2A  
top view**

metal  
bridge 36



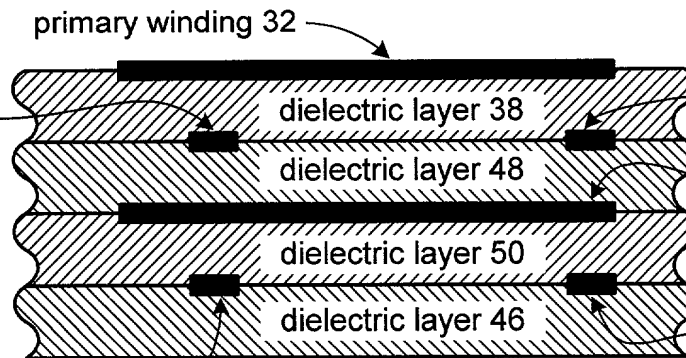
primary  
winding 32

metal  
bridge 34

dielectric  
layer 38

**FIG. 2B  
side view**

metal  
bridge 36



primary winding 32

dielectric layer 38

dielectric layer 48

dielectric layer 50

dielectric layer 46

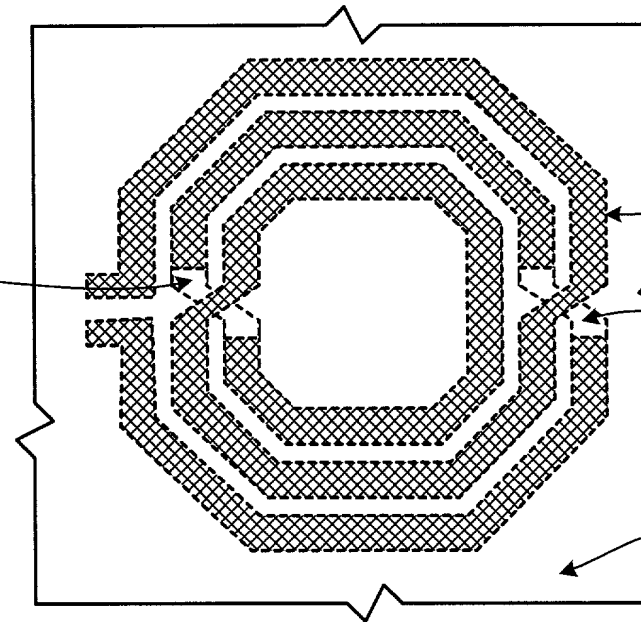
metal  
bridge 34  
secondary  
winding 40

metal  
bridge 42

metal bridge 44

**FIG. 2C  
bottom view**

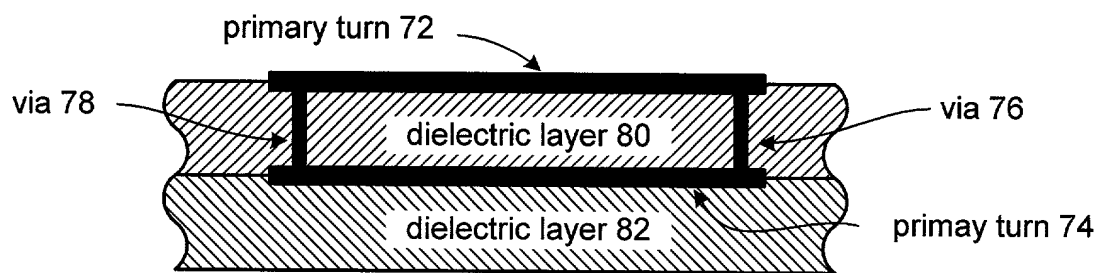
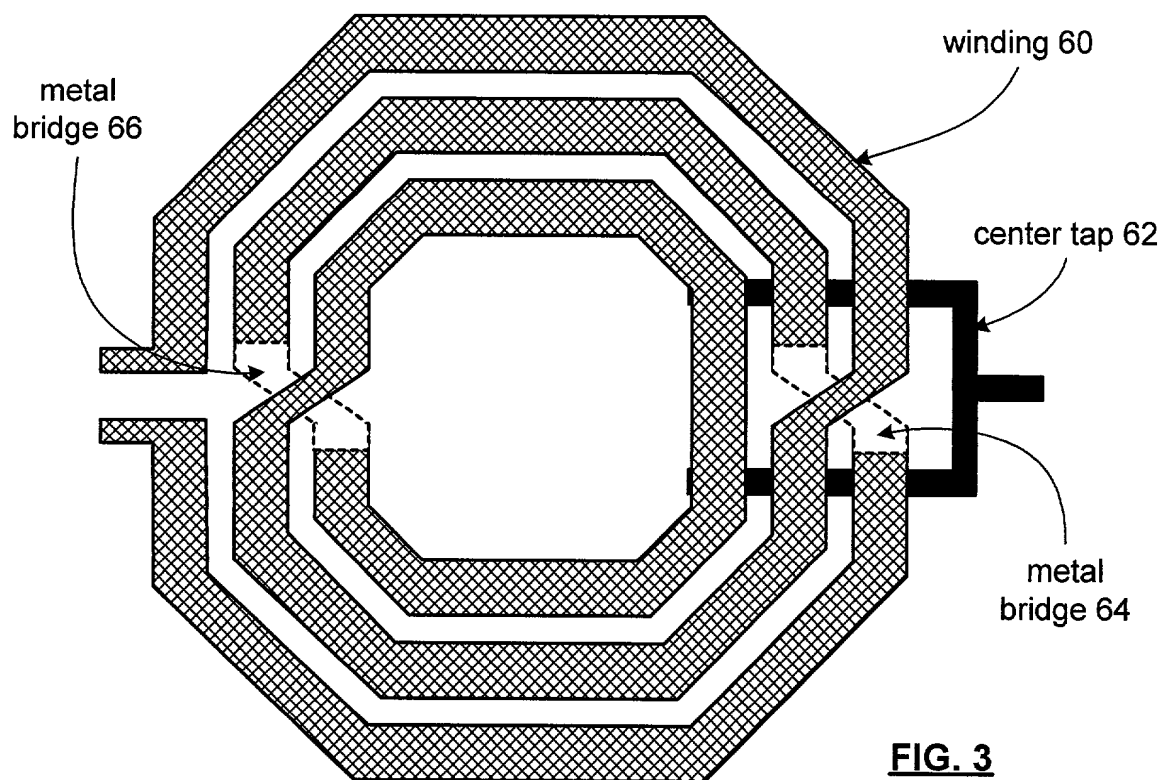
metal  
bridge 44



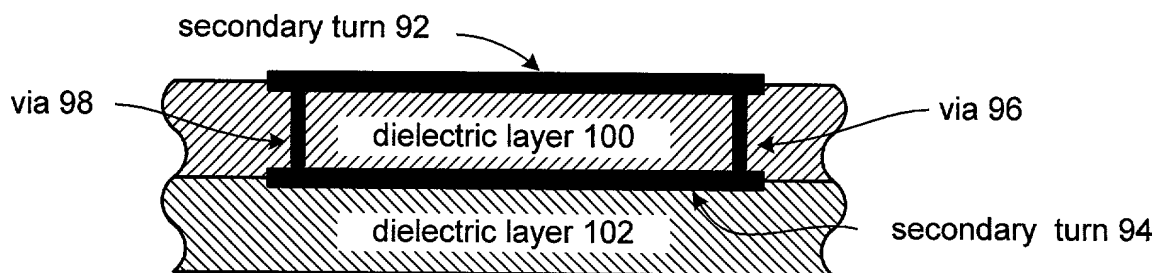
secondary  
winding 40

metal  
bridge 42

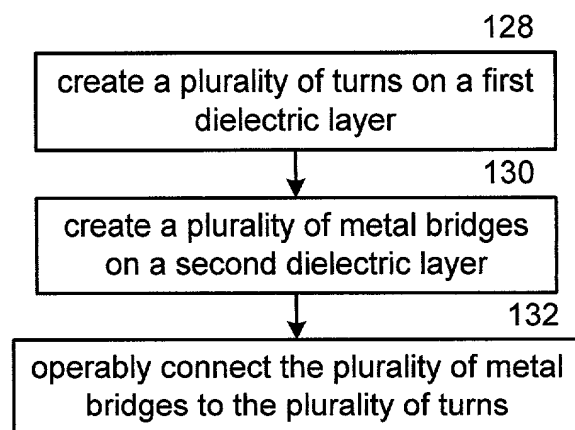
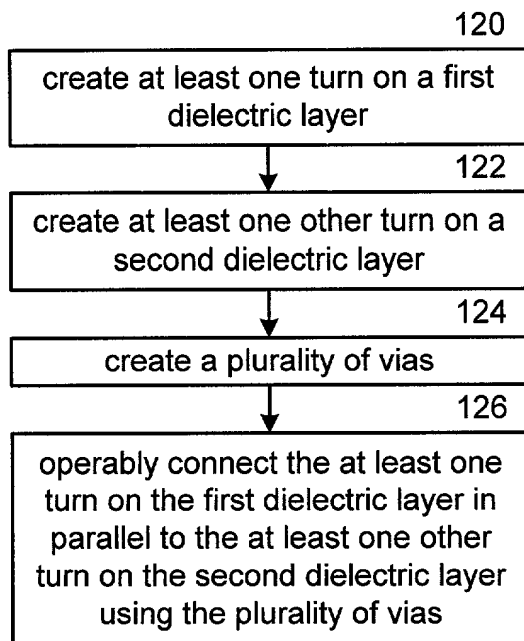
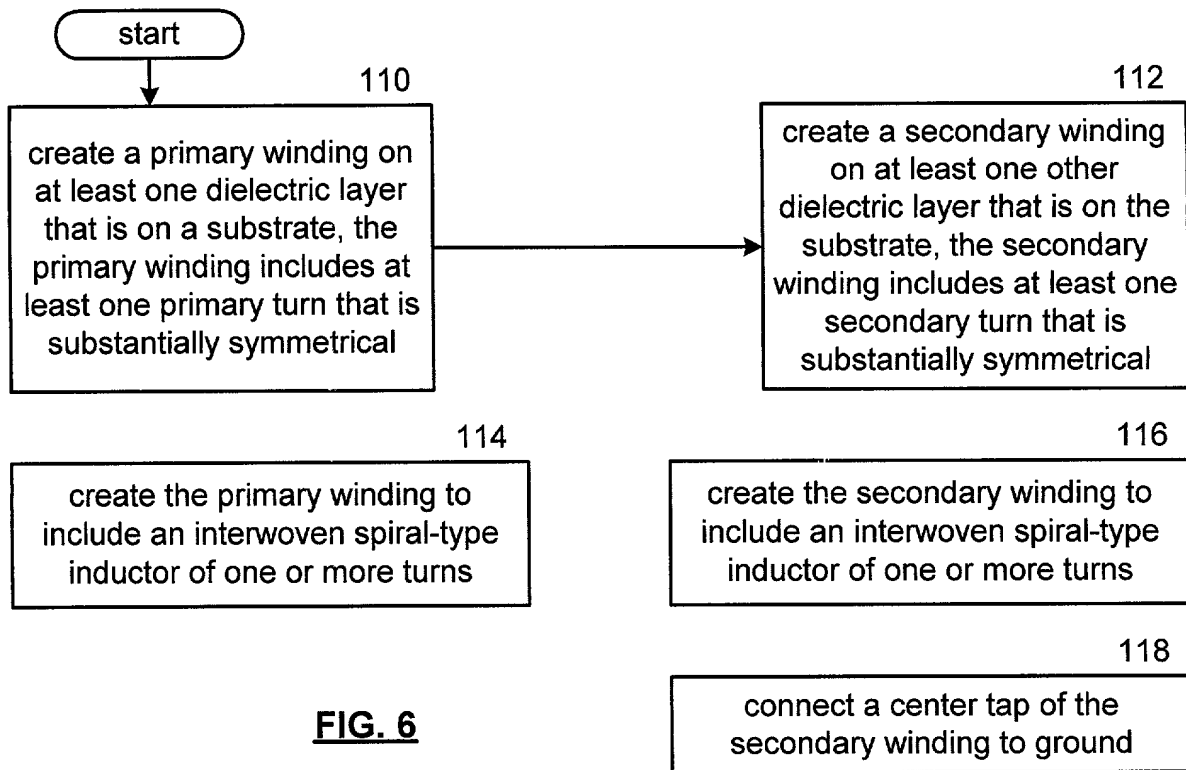
dielectric  
layer 46

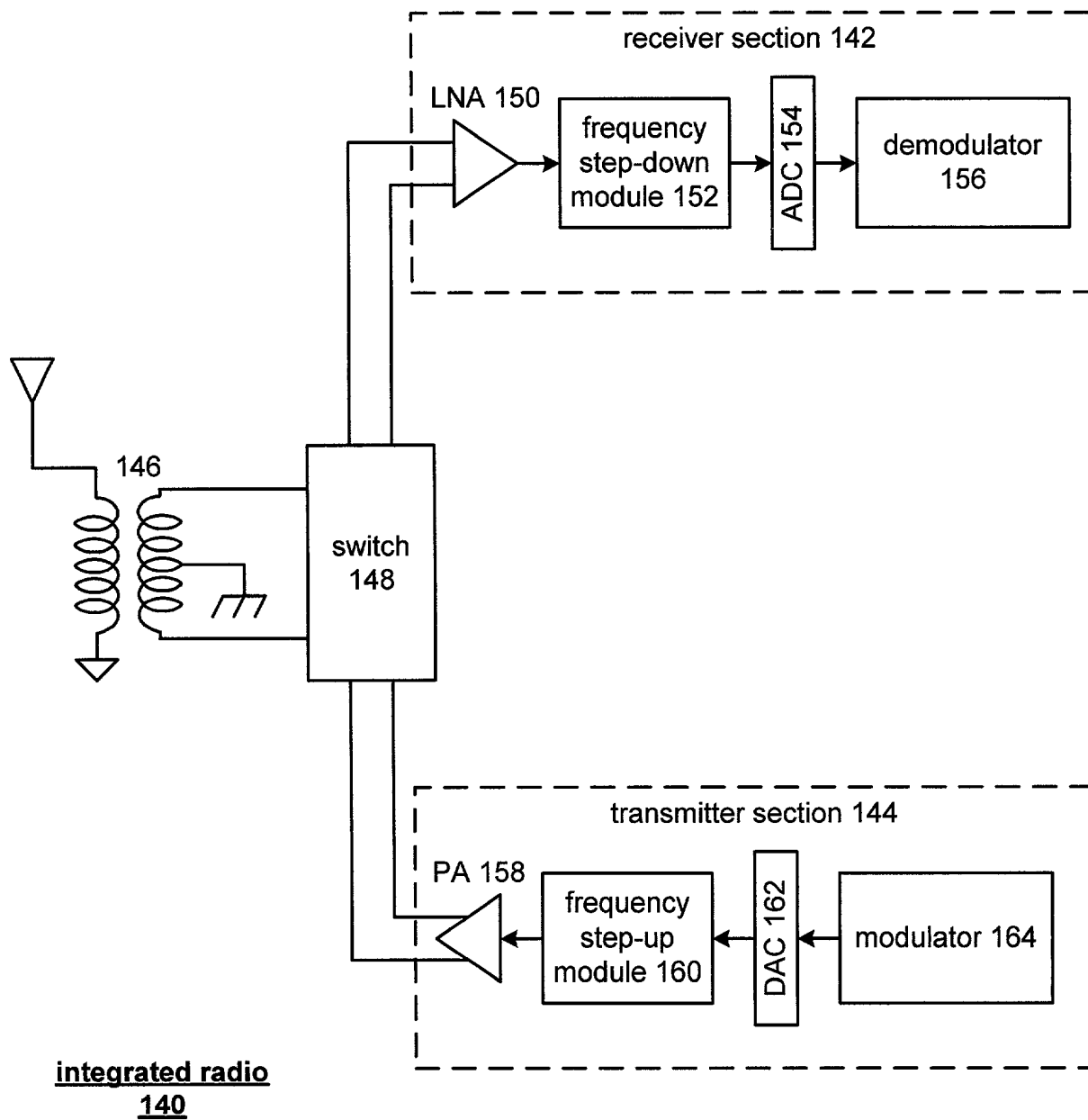


**FIG. 4**  
**primary winding 70**



**FIG. 5**  
**secondary winding 90**





**FIG. 9**